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HE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)

Mino Green

FOR

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SERIAL NO.

10/533,822

FILED

August 31, 2005

EXAMINER

Unknown

ART UNIT

2811

CONFIRMATION NO.

4177

ATTORNEY DOCKET NO.

KSTR 2 00004

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with 37 C.F.R. §§ 1.56, 1.97, 1.98 and MPEP § 609, applicant(s) submit(s) the following Disclosure Statement concerning art of which the applicant(s) is (are) aware. A copy of PTO-1449 is enclosed herewith.

This Information Disclosure Statement should not be construed to be an admission that any information referred to herein or submitted herewith is "prior art" or is considered to be material to patentability for this invention.

The United States Patent and Trademark Office OG Notice dated 12 October 2004 published a final rule revising 37 C.F.R. 1.98 dealing with the content of Disclosure Statements. Paragraph (a)(2) was revised to read in part, "A legible copy of: (i) Each foreign patent; (ii) Each publication or that portion which caused it to be listed, other than U.S. patents and U.S. patent application publications unless required by the

patents and published patent applications with this Information Disclosure Statement. In accordance with 37 C.F.R. §1.97(g) and (h), the filing of this Information Disclosure Statement should not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. §1.56(b) exists. Under § 1.98(a)(3), a concise explanation of relevance is required for information that is not in the English language. Accordingly, the English language documents have no further explanation. The cited and/or included documents were cited in the specification, in the M British and International search reports of related applications and other art enclosed is known to the inventors. Copies of the International and British Search Reports are enclosed. Consideration of the appropriate paragraph(s) indicated below is respectfully requested: Under § 1.97(b)(1), this WITHIN THREE MONTHS OF FILING: Information Disclosure Statement is being filed within three months of the filing date of the application (or date of entry of the national stage). Although it is believed no fee is necessary, any deficiency in fees should be handled as set forth below. BEFORE FIRST OFFICE ACTION: Under § 1.97(b)(3), this Information \boxtimes Disclosure Statement is being filed before the mailing date of a first Office Action on the merits. Although it is believed no fee is necessary, any deficiency in fees should be handled as set forth below. BEFORE FINAL ACTION, OR NOTICE OF ALLOWANCE, OR ACTION THAT CLOSES PROSECUTION/WITH STATEMENT: Under § 1.97(c)(1), this information shall be considered if filed before the mailing date of a final action, or a Notice of Allowance or action that otherwise closes prosecution in the application if accompanied by the statement: Under § 1.97(e)(1), the undersigned states: that each item of information contained in the Information Disclosure Statement was first cited in any communication from a foreign patent

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2. the fee in the amount of \$180.00 as required by §1.17(p). Accordingly, the necessary fee accompanies this Information Disclosure Statement, as set forth below.
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more prior applications from which the present application claims priority. These one or

more prior applications are identified in the papers accompanying the filing of this application.

Any payment due for the filing of this Information Disclosure Statement is authorized to be charged to a Credit Card. The appropriate form PTO-2038 is enclosed for this purpose. If the Credit Card is unable to be charged, please charge any and all fees or credit any overpayment to Deposit Account No. 06-0308.

It is respectfully requested that the attached document(s) be considered and officially cited in examination of this application.

Respectfully submitted,

FAY, SHARPE, FAGAN, MINNICH & McKEE, LLP

March 14, 2006 Date

I certify that this Information Disclosure

Jay F. Moldovanyi, Reg. No. 29,678 1000 Superior Avenue, Seventh Floor

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Substitute for form 1449/PTO Complete if Known **Application Number** 10/533.822 August 31, 2005 Filing Date INFORMATION DISCLOSURE First Named Inventor Mino Green STATEMENT BY APPLICANT(S) Art Unit 2811 Unknown **Examiner Name** KSTR 2 00004 Sheet 2 of 3 Attorney Docket No. U.S. PATENT DOCUMENTS **Publication Date** Name of Patentee or Cite Document No. Examiner Number-Kind Code (if known) Applicant of Cited Document MM-DD-YYYY Initials* No. US-6,334,939 01/01/2002 Zhou et al. BA Narang et al. BB US-6,337,156 01/08/2002 BC US-2004/0072067 04/15/2004 Minami et al. BD US-FOREIGN PATENT DOCUMENTS Name of Patentee or Foreign Patent Document Examiner Cite **Publication Date** Applicant of Cited Document Initials* No. MM-DD-YYYY Country Code-Number Kind Code (if known) (English Text) 10/25/2002 Sanyo Electric Co. Ltd. BE JP 2002-313319 10/21/2004 Sanyo Electric Co. Ltd. (English Text) BF JP 2004-296386 (English Abstract) 02/19/2002 University of Delft BG NL 1015956 (English Text) 07/01/1999 SRI International BH WO 99/33129 Imperial College of Science, Technology & BI WO 01/13414 02/22/2001 (English Text) Medicine WO 2004/042851 05/21/2004 Imperial College Innovations Limited (English BJ Text) BK OTHER - NON PATENT LITERATURE DOCUMENTS Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item Examiner Cite (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volumeissue number(s), Initials* No. publisher, city and/or country where published J. K. Niparko (Editor), "Cochlear Implants Technology", Pub., Lippincott Williams and Wilkins, BL Philadelphia, pp. 109 - 121 (2000). C.J. Wen et al., "Chemical Diffusion in Intermediate Phases in the Lithium-Silicon System", J. Solid State BM Chem., 37, pp. 271-278 (1981). W.J. Weydanz et al., "A RoomTemperature Study of the Binary Lithium-Silicon and the Ternary Lithium-BN Chromium-Silicon System for use in Rechargeable Lithium Batteries", J. Power Sources, 81-82, pp. 237-242 (1999). J-P. Colinge, "Silicon-on-Insulator Technology: Materials to VLSI", Kluwer Acad. Pub, Boston, Chapter 2, BO p. 38 (1991). Mino Green, "Quantum Pillar Structures on n⁺ Gallium Arsenide Fabricated Using 'Natural' Lithography", BP

		
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